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APPLICANTS Kazuhiro Fujikawa, Osaka-shi, JAPAN; Shin Harada, Osaka-shi, JAPAN; ** CONTINUING DATA ***** This application is a 371 of PCT/JP04/05649 04/20/2004 ** FOREIGN APPLICATIONS ***** JAPAN 2003-122240 04/25/2003 JAPAN 2003-144524 05/22/2003				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 01/31/2006				
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Verified and Acknowledged <input checked="" type="checkbox"/> Allowance <input type="checkbox"/> Initials Examiner's Signature <i>[Signature]</i> Initials <i>TL</i>		STATE OR COUNTRY JAPAN	SHEETS DRAWING 3	TOTAL CLAIMS 23
INDEPENDENT CLAIMS 2				
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TITLE Method of fabricating semiconductor device				
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